

**Deadline extended:
January 24, 2017**

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**The 23rd IEEE Real-Time Embedded
Technology & Applications Symposium**

**Work-in-Progress Session
(RTAS-WIP 2017)**

18 April, 2017 – Pittsburgh, PA, USA

The **Work-in-Progress (WiP) session at RTAS 2017** is dedicated to new and on-going research in the field of real-time and embedded systems. Authors are invited to submit short papers describing ongoing, unpublished work in all areas of real-time and embedded technology, including applications, systems, tools, methodologies, foundations, wireless sensor networks, and hardware-software co-design. In keeping with the spirit of the main symposium, submissions with an emphasis on systems and application aspects are especially encouraged.

The WiP session provides researchers and developers with an opportunity to discuss evolving and early-stage ideas, new research directions, to review current trends in the area and to receive feedback from the real-time systems community at large.

Accepted papers will be published in the same proceedings as the main conference (no longer a separate booklet) and **are indexed by IEEE** (they receive a DOI and appear in IEEExplore). All submissions should be in the form of short papers of 2 to 4 pages in the IEEE 10-point, two-column conference format, including all references and appendices. Titles should be in the format "**Work-in-Progress: WiP_Title**".

Authors of accepted papers are expected to give a brief presentation in the work-in-progress session of the conference and also showcase their work at the poster session. By submitting a paper, the authors agree that, if accepted, at least one author will register for the conference and present the work in person at the Work-in-Progress session.